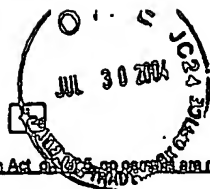


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				Application Number	10/805,809
				Filing Date	March 22, 2004
				First Named Inventor	Madan et al.
				Group Art Unit	2827
Examiner Name	S. MAI				
Sheet	1	of	1	Attorney Docket No.	TI-36818

U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No.¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code² (if known)			
JH	AA	6,048,928		Takata	04/04/2000	Entire Document
JH	AB	6,208,550	B1	Kim	03/27/2001	Entire Document
JH	AC	6,487,103	B2	Yamamoto et al.	11/26/2002	Entire Document
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JH	AJ	10/866,834		Madan et al.	06/14/2004	Entire Document

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FOREIGN PATENT DOCUMENTS							
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Exam. Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
JH	CA	"A Bit-Line GND Sense Technique for Low-Voltage Operation FeRAM", SHOICHIRO KAWASHIMA, TORU ENDO, AKIRA YAMAMOTO, KEN'ICHI NAKABAYASHI, MITSU HARU NAKAZAWA, KEIZO MORITA and MASAKI AOKI, 2001 Symposium on VLSI Circuits Digest of Technical Papers, pgs. 127 & 128.	
JH	CB	"A Survey of Circuit Innovations in Ferroelectric Random-Access Memories", ALI SHEIKHOESLAMI and P. GLENN GULAK, Proceedings of the IEEE, Volume 88, No. 5, May 2000, pp. 667-689.	
	CC		
	CD		
	CE		

Examiner Signature	SON MAI	Date Considered	01/31/06
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